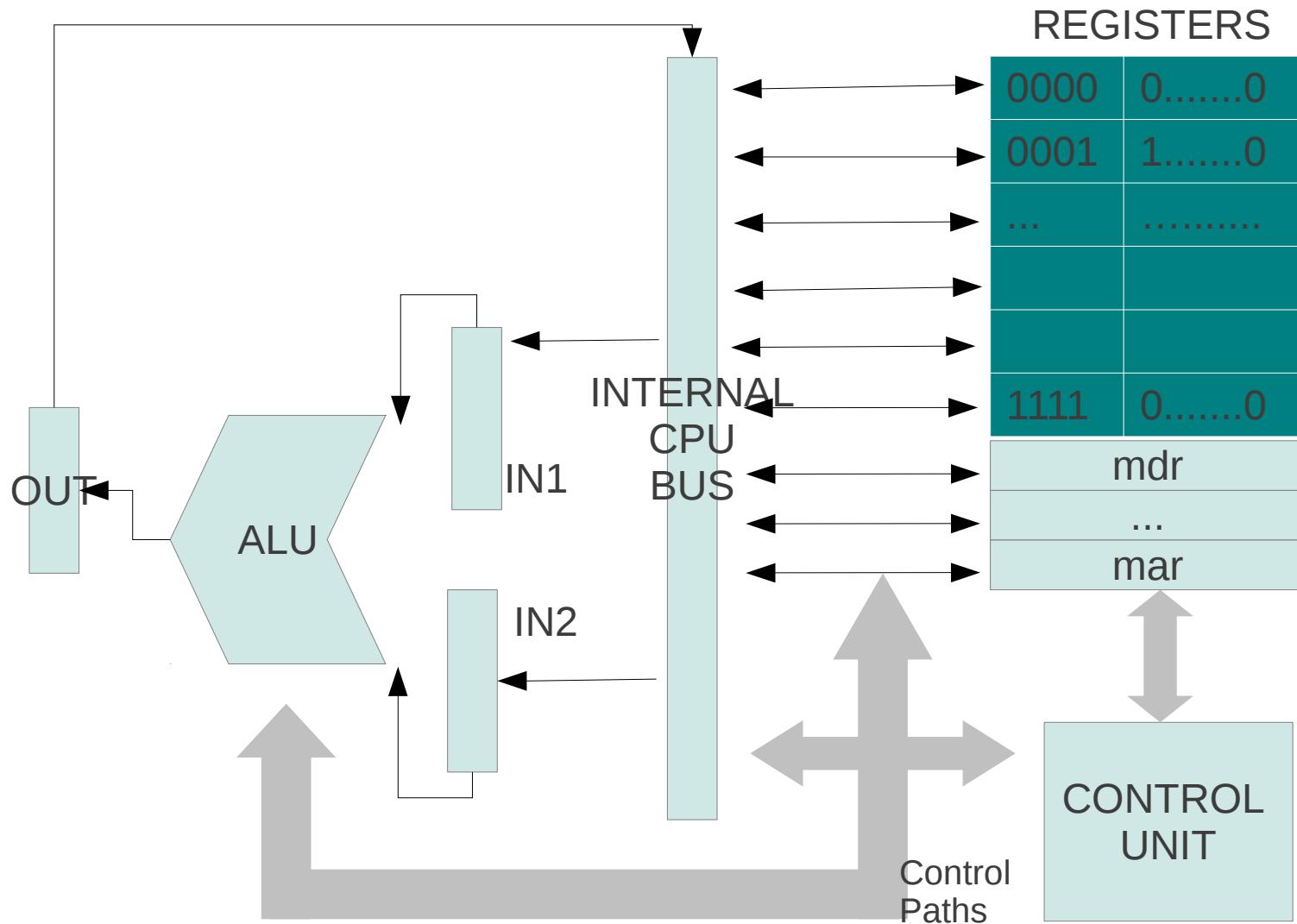


# Architettura CPU con registri ad uso generale



# Wombat2 instructions

INSTRUCTION	MEANING	COMMENTS
readR \$reg1	Read $n \rightarrow \$reg1$	Input from keyboard in $\$reg1$
writeR \$reg1	$\$reg1 \rightarrow$ output	Write value of $\$reg1$
multiplyR \$reg1 \$reg2 \$reg3	$\$reg1 * \$reg2 \rightarrow \$reg3$	Multiply contents of two registers
divideR \$reg1 \$reg2 \$reg3	$\$reg1 / \$reg2 \rightarrow \$reg3$	Divide contents of two registers
subtractR \$reg1 \$reg2 \$reg3	$\$reg1 - \$reg2 \rightarrow \$reg3$	Subtract contents of two registers
addR \$reg1 \$reg2 \$reg3	$\$reg1 + \$reg2 \rightarrow \$reg3$	Add contents of two registers
loadR \$reg1 \$addr	$\text{Mem}[\text{addr}] \rightarrow \$reg1$	Load word from memory in $\$reg1$
storeR \$reg1 \$addr	$\$reg1 \rightarrow \text{Mem}[\text{addr}]$	Store word in memory from $\$reg1$
mtcrR \$reg1	$\$reg1 \rightarrow \text{CR}$	Move to condition register (for conditional jumps)
jumpzR \$addr	If $\text{CR} == 0$ jump to $\$addr$	Conditional jump ( $\text{CR} == 0$ )
jmpnR \$addr	If $\text{CR} < 0$ jump to $\$addr$	Conditional jump ( $\text{CR} < 0$ )