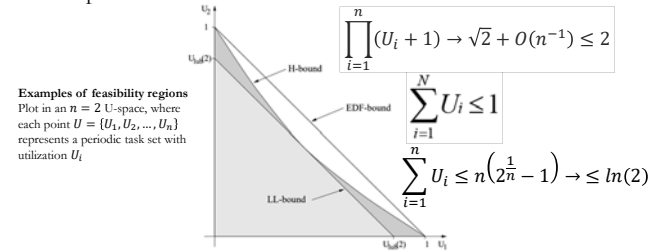


6.a Ramifications of schedulability analysis

Credits to Marco Panunzio, PhD
(marco.panunzio@thalesaleniaspace.com)

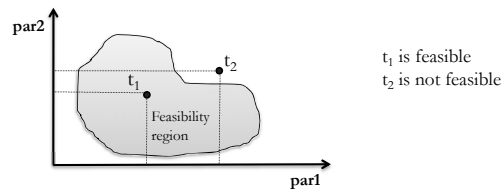
Example

- *Hyperbolic bound* [Bini & Buttazzo, ECRTS, 2001] improves the Liu & Layland utilization test for RM
 - It helps prove that RM achieves 100% utilization when *all pairs* of periods in the task set are in harmonic relation



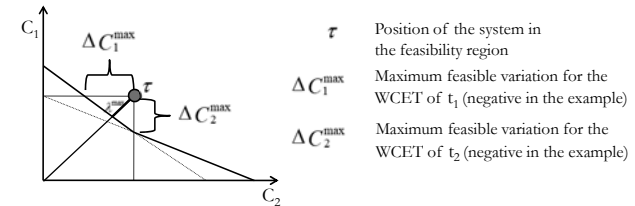
Feasibility region

- A topological space that represents the set of feasible systems with respect to workload model parameters
 - N-dimensional space for N-parameter analysis
 - Specific to the schedulability tests in use
 - Helps visualize the meaning of feasibility geometrically

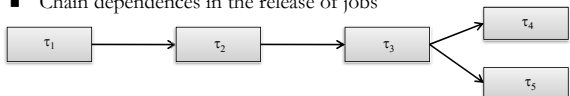


Sensitivity analysis

- Investigates the parameter changes in a real-time system that may possibly
 - Improve the goodness of fit of an already feasible system
 - Make feasible an infeasible system



Transactions (precedence chains) /1

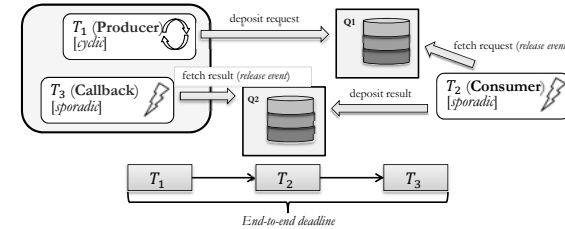
- Causal relations between activities
 - They allow considering relevant information not captured by classic workload models
 - Chain dependences in the release of jobs
- 
- ```

 graph LR
 tau1[τ1] --> tau2[τ2]
 tau2 --> tau3[τ3]
 tau3 --> tau4[τ4]
 tau3 --> tau5[τ5]

```
- Originated in the analysis of distributed systems, where understanding offsets contains the pessimism of release jitter
    - Also useful for the analysis of “collaboration (release) patterns” employed for single-CPU systems

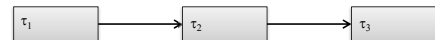
## Example: Ravenscar call-back

- The “call-back pattern” helps realize indirect in-out interactions between tasks in Ravenscar systems

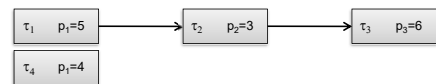


- The feasibility of the *end-to-end response time* against the corresponding deadline is the matter of interest here (!)

## Transactions /2

- Two main kinds of dependence are of interest here
    - *Direct precedence* relation (e.g., producer-consumer)
      - $\tau_2$  cannot proceed until  $\tau_1$  completes
- 
- ```

    graph LR
      tau1[τ1] --> tau2[τ2]
      tau2 --> tau3[τ3]
    
```
- *Indirect priority* relation
 - τ_4 does not suffer interference from τ_3 (under FPS and synchronous release of τ_1 and τ_4 for priorities increasing with values)



Example: classic RTA results

Id	Task	T_i	C_i	Priority	Blocking
τ_1	Producer (periodic)	40	10	4	$B_1 = 2$
τ_2	Consumer (sporadic)	40	10	2 (L)	$B_2 = 0$
τ_3	Call-back (sporadic)	40	5	5 (H)	$B_3 = 2$

Q1 Ceiling = $\max(P_1, P_2) = 4$

Q2 Ceiling = $\max(P_2, P_3) = 5$

Classic RTA

$R_1 = 17$
 $R_2 = 25$
 $R_3 = 7$

This misses out completely that τ_3 is to be preceded by τ_2 and τ_1 (!)

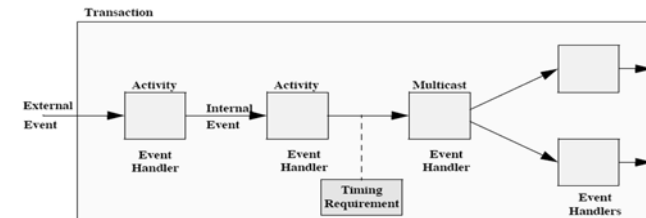
$$R_i = C_i + B_i + \sum_{j \in hp(i)} \left\lceil \frac{R_j}{T_j} \right\rceil C_j$$

MAST

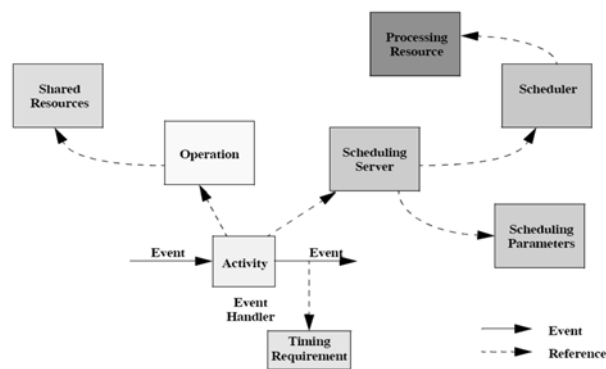
- Modeling and Analysis Suite for Real-Time Systems (MAST, <http://mast.unican.es>)
 - Developed at University of Cantabria, Spain
 - Open source
 - Implements several analysis techniques
 - For uniprocessor and distributed (no-shared memory) processor systems
 - Under FPS or EDF

MAST: transaction

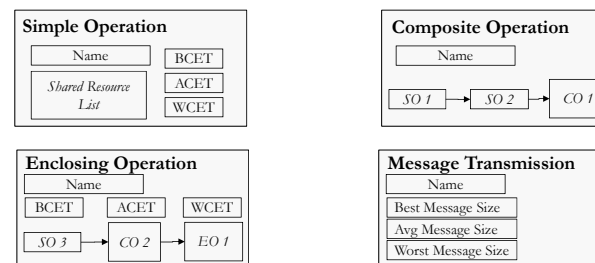
- To model causal relations between activities
 - Triggered by external events
 - Periodic, sporadic, aperiodic, etc...



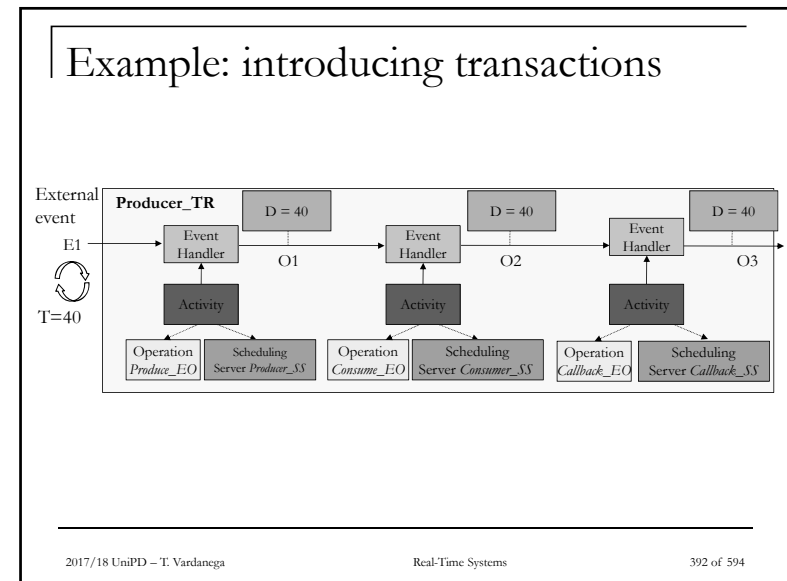
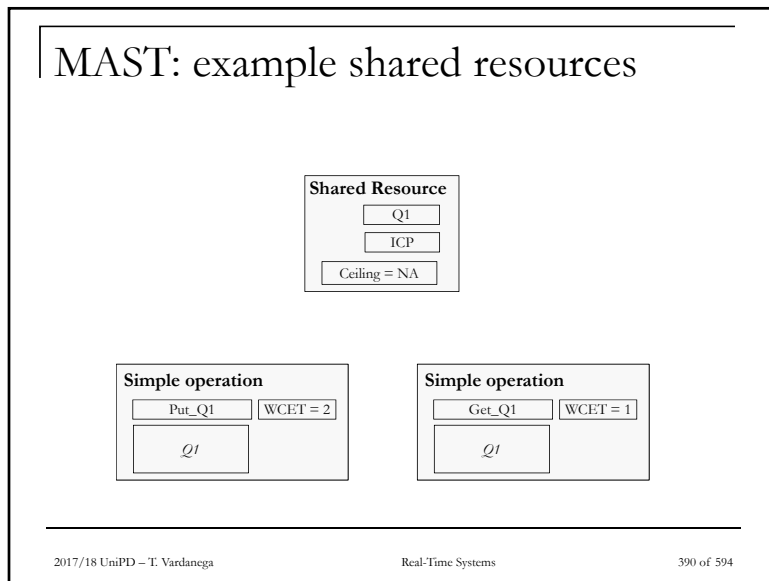
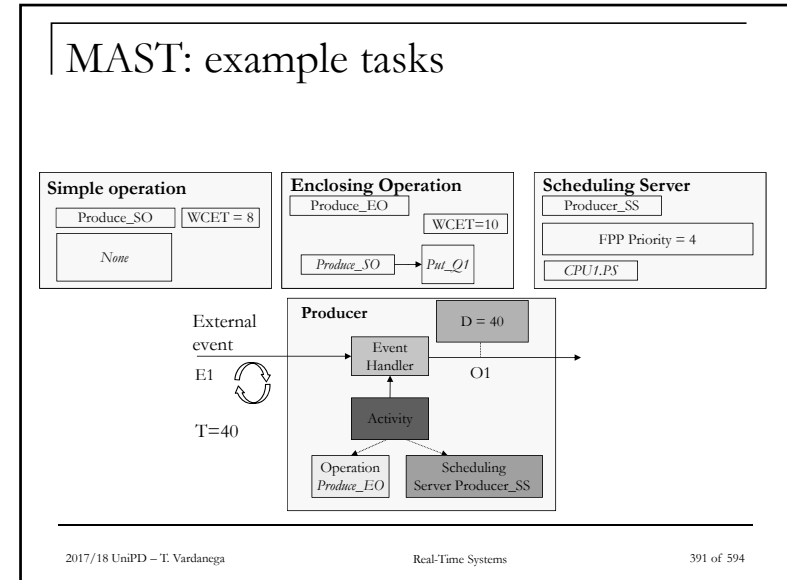
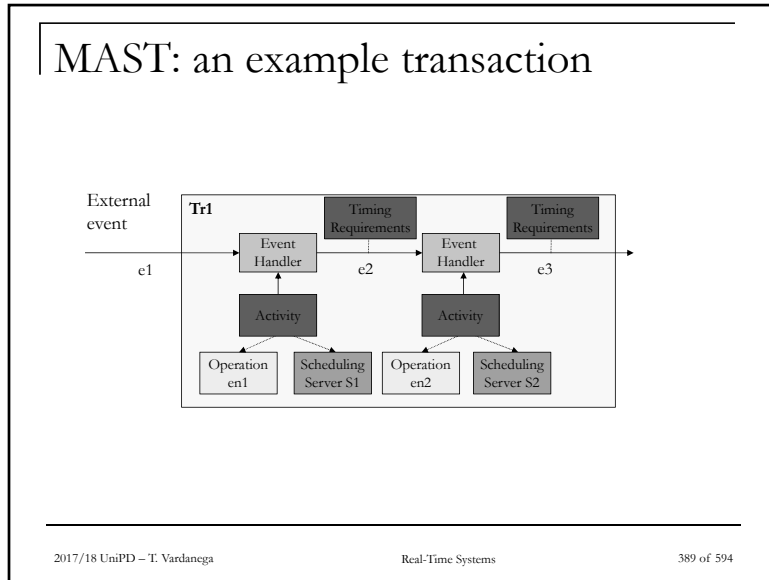
MAST: real-time model



MAST: operations



- The real-time model includes the description of all the operations in the system



Example: end-to-end analysis

Id	Task	T_i	C_i	Priority	Blocking
τ_1	Producer (periodic)	40	10	4	$B_1 = 2$
τ_2	Consumer (sporadic)	40	10	2 (L)	$B_2 = 0$
τ_3	Call-back (sporadic)	40	5	5 (H)	$B_3 = 2$

Q1 Ceiling = $\max(P_1, P_2) = 4$

Q2 Ceiling = $\max(P_2, P_3) = 5$

Classic RTA

$$R_1 = 17$$

$$R_2 = 25$$

$$R_3 = 7$$

Precedence and offset-based RTA

$$R_1 = 12$$

$$R_2 = 20, O_2 = R_1^{best}, J_2 = R_1 - R_1^{best}$$

$$R_3 = 27 \leftarrow \text{Relative to the beginning of the transaction, not knowing the best case}$$

$$R_i = C_i + B_i + \sum_{j \in hp(i)} \left\lceil \frac{R_i - O_j + J_j + O_i + J_i}{T_j} \right\rceil C_j - O_i + J_i$$

Summary

- Feasibility region
- Advanced utilization tests
- Sensitivity analysis
- Transactions
- Example with MAST