

## 4.b Under the hood

### Context switch

- The time and space overhead incurred on preemption should be accounted for in schedulability analysis
- Under preemption, every single job incurs *at least two* context switches
  - At activation, to load (or install) its execution context
  - At completion, to store (clean up) the same
- The ensuing cost should be charged to the job's WCET
  - Which requires knowing the internal timing behavior of the run-time system

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279 of 538

### Priority levels /1

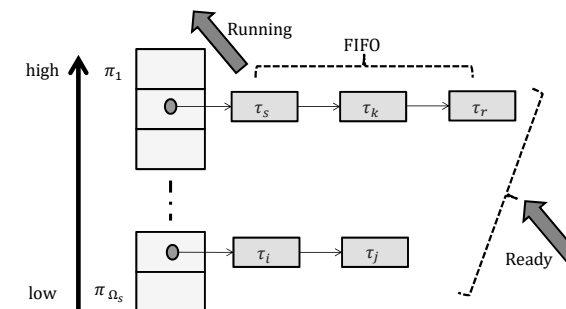
- The scheduling techniques that we have studied assume jobs to have *distinct* priorities
  - Concrete systems may not always have sufficient priorities
  - Jobs may have to *share* priority levels
  - For jobs at the same level of priority, dispatching may be FIFO or round-robin: the former is more fit for real-time
- If priority levels are shared, we have a worst-case situation to contemplate in the analysis
  - That job  $J$  be released immediately *after* all other jobs at its level of priority

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280 of 538

### Example: FIFO within priorities



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281 of 538

### Priority levels /2

- Let  $S(i)$  denote the set of jobs  $\{J_j\}$  with  $\pi_j = \pi_i$ , excluding  $J_i$  itself
- The time demand equation for  $J_i$  in the interval  $0 < t \leq \min(D_i, p_i)$  becomes
 
$$\omega_{i_1}(t) = e_i + B_i + \sum_{S(i)} e_{j \in S(i)} + \sum_{k=1, \dots, i-1} \left\lceil \frac{\omega_{i_1}(t)}{p_k} \right\rceil e_k$$
- This obviously worsens  $J_i$ 's response time
  - Yet, the impact in terms of **schedulability loss** at system level may not be that bad (wait and see ...)

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### Priority levels /4

- When the *assigned priorities*  $\mathcal{R} = [1, \dots, \Omega_n]$  exceed the *available priorities*  $\mathcal{H} = [\pi_1, \dots, \pi_{\Omega_s}]$ , ( $\|\mathcal{R}\| > \|\mathcal{H}\|$ ), we need a  $\Omega_n: \Omega_s$  mapping function that collapses the former into the latter (aka the *priority grid*)
  - All assigned priorities  $\geq \pi_1$  will take value  $\pi_1$
  - For  $1 < k \leq \Omega_s$ , the assigned priorities in the range  $(\pi_{k-1}, \pi_k]$  will take value  $\pi_k$
- Two main techniques address this problem
  - Uniform mapping**
  - Constant ratio mapping** [Lehoczky & Sha, 1986]

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### Priority levels /5

- Uniform mapping** ( $Q = \left\lfloor \frac{\Omega_n}{\Omega_s} \right\rfloor$ )
  - $\pi_k \leftarrow [k, \dots, kQ], \pi_{k+1} \leftarrow [kQ + 1, \dots, (k + 1)Q]; k = 1, \dots, \Omega_s - 1$
  - Example**  
 $\Omega_n = 9, \Omega_s = 3, Q = \frac{9}{3} = 3, \pi_1 = 1, \pi_2 = 2, \pi_3 = 3$   
 $\pi_1 \leftarrow [1..3], \pi_2 \leftarrow [4..6], \pi_3 \leftarrow [7..9]$
- Constant ratio mapping (CRM)**
  - Collapses subsets of  $\mathcal{R}$  into the  $\pi_i$  values of  $\mathcal{H}$  by keeping the ratio  $g = \frac{(\pi_{i-1} + 1)}{\pi_i}$  constant for  $i = 2, \dots, \Omega_s$ , to favor higher-priority jobs
  - Example (same as above)** ( $g = \frac{1}{2}$ )  
 $\pi_1 = 1, \pi_2 = 4, \pi_3 = 10 \Rightarrow \pi_1 \leftarrow [1], \pi_2 \leftarrow [2..4], \pi_3 \leftarrow [5..9]$

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### Priority levels /6

**Uniform mapping**

**Constant ratio mapping**

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### Priority levels /7

- Lehoczky & Sha showed that CRM degrades the schedulable utilization of RMS *gracefully*
  - For large  $n$ , with  $D_i = p_i \forall i$ , and  $g = \min_{2 \leq j \leq \Omega_s} \frac{(\pi_{j-1} + 1)}{\pi_j}$ , the CRM's schedulable utilization approximates
 
$$f(g) = \begin{cases} \ln(2g) + 1 - g, & g > \frac{1}{2} \\ g, & g \leq \frac{1}{2} \end{cases}$$
- The  $\frac{f(g)}{\ln(2)}$  ratio represents the **relative schedulability** of CRM in relation to RMS' utilization bound
  - Example:** for  $\Omega_s = 256, \Omega_n = 100,000$ , and the corresponding  $g$ , CRM's relative schedulability is 0.9986
- Hence, 256 priority levels should suffice for RMS

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### Real-time operating systems /1

- The RTOS knows all tasks: their jobs are the unit of CPU assignment
  - Tasks issue jobs: scheduling and dispatching applies to jobs
  - The *scheduler* decides which job gets the CPU
  - The *dispatcher* gets jobs to run and operates context switches
- One **Task Control Block (TCB)** per task is stored in RAM
  - The insertion of a task in a state queue (e.g., ready) is made by placing a pointer from the queue to the corresponding TCB
  - The end-of-life disposal of a task requires removing its TCB and releasing all of its memory (its stack and its global data in the heap)
  - This is onerous and suggests preferring *infinite* tasks

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### Task control block (example)

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### Real-time operating systems /2

- Tasks may be realized as specialized primitive entities that live within the RTOS
- Then the *model of computation* is determined by the RTOS
  - Outside or inside of the programming language, dependent on the binding of it with the RTOS
  - Inside, for the Ada Ravenscar Profile
- Otherwise, the MoC may be defined at the application level using with generic support from the RTOS API
  - In that case it is the user's responsibility to ensure that the eventual execution semantics conforms with the assumptions made in schedulability analysis

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## Real-time operating systems /3

### ■ Periodic task

- An RTOS thread that hangs on a periodic *suspension point*
  - After release, it executes the application-code of the job and then makes a suspension call

### ■ Sporadic task

- An RTOS thread whose suspension point is not released periodically but with *guaranteed* minimum distance
  - After release, it executes the job and then calls a wait-for-event service

### ■ Aperiodic task

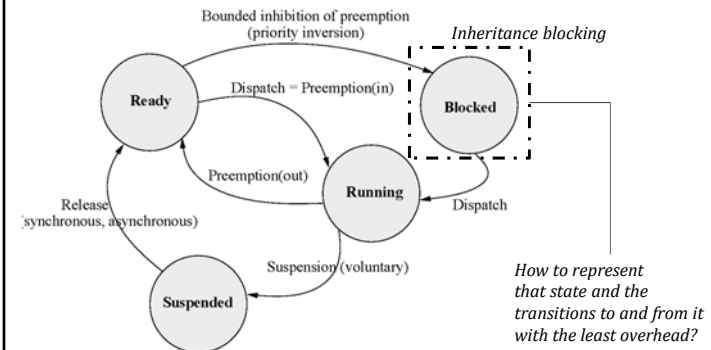
- Indistinguishable from the rest other than its being placed in a server's *backlog queue* and not in the ready queue

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290 of 538

## Task states /1



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291 of 538

## Task states /2

### ■ Tasks enter the *suspended* state only voluntarily

- By making a primitive invocation that causes them to hang on a periodic / sporadic suspension point

### ■ The RTOS needs specialized structures to handle the distinct forms of suspension

- A *time-based queue* for periodic suspensions
- An *event-based queue* for sporadic suspensions
  - But “someone” (IoC in the OOD solution we saw earlier) shall assure minimum separation between subsequent releases (!)

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292 of 538

## Time management /1

### ■ A system clock consists of

- A periodic counting register
  - Automatically reset to the *tick size* every time it reaches the *triggering edge* and triggers the *clock tick*
- Composed of
  - A *HW part* automatically decremented at every clock pulse and a *SW part* incremented by the handler of the clock tick
  - A queue of time events fired in the interval, whose treatment is pending
- And an (immediate) interrupt handling service

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293 of 538

## Time management /2

- The frequency of the clock tick fixes the *resolution* (granularity) of the *software part* of the clock
  - The resolution should be an integer divisor of the tick size so that the RTOS may perform tick scheduling at every  $N$  clock ticks
  - So that we have more frequent time-service interrupts and less frequent ( $\frac{1}{N}$ ) clock interrupts
    - Time-service interrupts maintain the system clock
    - Clock interrupts are used for scheduling

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294 of 538

## Time management /3

- The clock resolution is an important design parameter
  - The finer the resolution the better the clock accuracy and the larger the time-service interrupt overhead
- There is delicate balance between the clock accuracy needed by the application and the clock resolution that can be afforded by the system
  - Latency is intrinsic in any query made by a task to the software clock
    - E.g., 439 clock cycles in ORK for the Leon microprocessor (cf. [www.dit.upm.es/~ork/](http://www.dit.upm.es/~ork/))
- The resolution cannot be finer-grained than the maximum latency incurred in accessing the clock (!)

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295 of 538

## Time management /4

- Beside periodic clocks, RTOS may also support *one-shot timers* aka interval timers
  - They operate in a programmed (non-repetitive) way
- The RTOS scans the queue of the programmed time events to set the time of the next interrupt due from the interval timer
  - The resolution of the interval timer is limited by the time overhead of its handling by the RTOS
    - E.g., 7,061 clock cycles in ORK for Leon

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296 of 538

## Time management /5

- The accuracy of time events is the difference between the time of event occurrence and the time programmed
- It depends on three fundamental factors
  - The frequency at which the time-event queues are inspected
    - If interval timers were *not* used, this would correspond to the period of time-service interrupts
  - The policy used to handle the time-event queues
    - LIFO vs. FIFO
  - The time overhead cost of handling time events in the queue
- It follows that the release time of periodic tasks is exposed to jitter (!)

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297 of 538

## The scheduler /1

- This is a distinct part of the RTOS that does **not** execute in response to explicit application invocations
  - Other than when using cooperative scheduling
- The scheduler acts every time the ready queue changes
  - The corresponding time events are termed *dispatching points*
- When the MoC is defined outside of the programming language and the RTOS is MoC-agnostic, scheduler “activation” is periodic in response to *clock interrupts*

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298 of 538

## The scheduler /2

- At every clock interrupt, the scheduler must
  - Increment the execution time budget counter of the running job to support time-based scheduling policy (e.g., LLF)
  - Possibly manage the queue of time-based events pending
  - Possibly manage the ready queue
- The  $\geq 10\text{ ms}$  period (aka *tick size*) typical of general-purpose operating systems is *too coarse* for RTOS
  - But higher frequency incurs larger overhead
- The scheduler should support event-driven execution with *minimum latency*

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299 of 538

## Tick scheduling /1

- The scheduler can be *event-driven* only if the MoC is defined *within* the application programming language
  - The scheduler always immediately executes on the occurrence of a *scheduling event* (aka dispatching point)
  - If it was so then we could assume that a job is placed in the ready queue exactly at its release time
- Several schedulers are *time-driven* instead
  - They make scheduling decisions on the arrival of periodic clock interrupts, with *no* relation to application events
  - This mode of operation is termed *tick scheduling*

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300 of 538

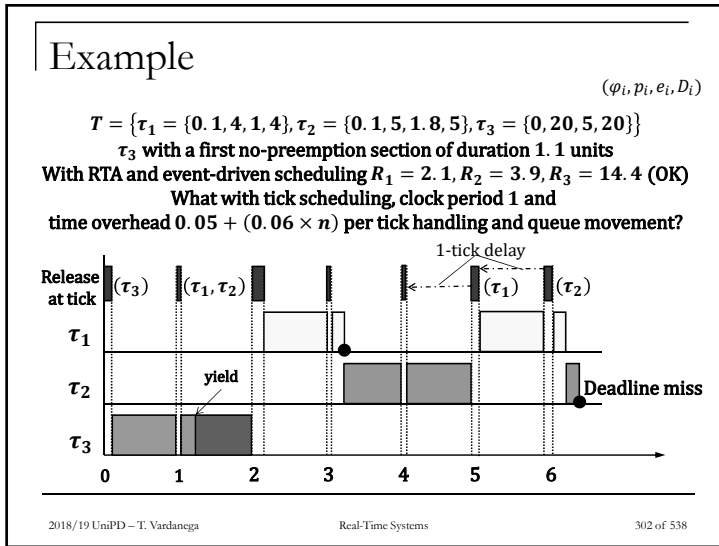
## Tick scheduling /2

- The tick scheduler may acknowledge a job’s release time up to one tick *later* than it arrived
  - This delay has negative impact on the job’s response time
  - We must assume a logical place where jobs in the “*release time arrived but not yet acknowledged*” state are held
  - The time and space overhead of transferring jobs from that logical place to the ready queue is not null and must be accounted for in the schedulability test together with the time and space overhead of handling clock interrupts

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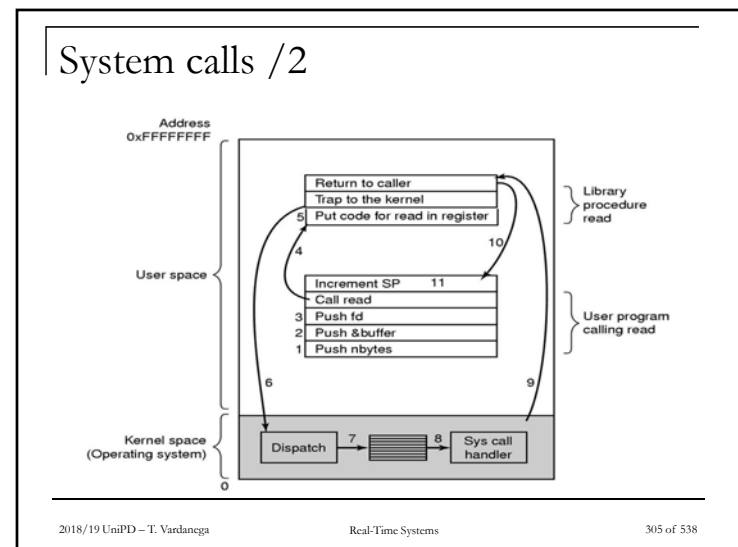
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301 of 538



- ### Tick scheduling /3
- The effect of tick scheduling is captured in RTA for job  $J_i$  by
    - Introducing a notional task  $\tau_0 = (p_0, e_0)$  with highest priority, to account for the  $e_0$  cost of handling clock interrupts with period  $p_0$
    - For all jobs  $J_k : \pi_k \geq \pi_i$ , adding to  $e_k$  the time overhead  $m_0$  due to moving each of them to the ready queue
      - $(K_k + 1)$  times for the  $K_k$  times that job  $J_k$  may self suspend
    - For every job  $J_l : \pi_l < \pi_i$ , introducing a distinct notional task  $\tau_\gamma = (p_l, m_0)$  to account for the time cost of moving  $J_l$  to the ready queue
    - Computing  $B_i(np)$  as function of  $p_0$ :  $J_i$  may suffer up to  $p_0$  units of delay after becoming ready even without non-preemptive execution
      - $B_i(np) = (\lceil \max_k \frac{\theta_k}{p_0} \rceil + 1)p_0$  before including non-preemption
      - Where  $\theta_k$  is the maximum time of no-preemption execution by any job  $J_k$
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- ### System calls /1
- The most part of RTOS services are executed in response to direct or indirect invocations by tasks
    - These invocations are termed *system calls*
  - For safety reasons, the system call APIs are *not* directly visible to the application
    - System calls are normally hidden in procedures exported to the programming language by compiler libraries
    - Those library procedures do all of the preparatory work for correct invocation of the designated system call on behalf of the application
  - Thanks to that “hiding”, the OS does *not* share memory with the application
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## System calls /3

- In embedded systems instead, the RTOS and the application often *share* memory
- Real-time embedded applications are more trustworthy
  - Hence, we do not want to pay the space and time overhead arising from *address space separation*
- The RTOS must then protect its own data structures from the risk of race condition
  - RTOS services must therefore be non-preemptable

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306 of 538

## I/O issues

- The I/O subsystem of a real-time system may require its own scheduler
  - It may be an *active* resource, after the taxonomy we saw in the introduction material
- Simple methods to access an I/O resource use
  - Run-to-completion non-preemptive FIFO semantics
  - Or some kind of time-division scheme
    - Non-preemptive quantized
- Or else use priority-driven scheduling as for CPU scheduling
  - RM, EDF, LLF can be used to schedule I/O requests

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307 of 538

## Interrupt handling /1

- HW interrupts are the most efficient manner for the processor to notify the application about the occurrence of external events that need attention
  - E.g., *asynchronous* completion of I/O operations delegated to external units like DMA (direct memory access)
- Frequency and computational load of the interrupt handling activities vary with the interrupt source

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308 of 538

## Interrupt handling /2

- For better efficiency, the interrupt handling service is subdivided in an *immediate* part and a *deferred* part
  - The immediate part executes at the level of interrupt priorities, above all SW priorities
  - The deferred part executes as a normal SW activity
- The RTOS must allow the application to tell which code to associate to either part
  - Interrupt service can also have a *device-independent* part and a *device-specific* part

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309 of 538



### Interrupt handling /3

- When the HW interface asserts an interrupt, the processor saves state registers (e.g., PC, PSW) in the interrupt stack and jumps to the address of the needed *interrupt service routine* (ISR)
  - At this time, interrupts are *disabled* to prevent race conditions on arrival of further interrupts
  - Interrupts arriving at that time may be lost or kept pending (depending on the HW)
- Interrupts operate at an assigned level of priority so that interrupt service incurs scheduling if interrupts nest

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310 of 538

### Interrupt handling /4

- Depending on the HW, the interrupt source is determined by *polling* or via an *interrupt vector*
  - Polling is HW independent hence more generally applicable but it increases latency of interrupt service
  - Vectoring needs specialized HW but it incurs less latency
- Once the interrupt source is determined, registers are restored and interrupts are enabled again

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311 of 538

### Interrupt handling /5

- The worst-case latency incurred on interrupt handling is determined by the time needed to
  1. Complete current instruction
  2. Save registers
  3. Clear the pipeline
  4. Acquire the interrupt vector
  5. Activate the trap
  6. Disable interrupts (so that the immediate part of the ISR can execute at the highest priority)
  7. Save the context of the interrupted task
  8. Identify the interrupt source and jump to the corresponding ISR
  9. Begin execution of the selected ISR

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312 of 538

### Interrupt handling /6

- To reduce *distributed overhead*, the deferred part of the ISR must be preemptable
  - Hence it must execute at software priority
- But it still may directly or indirectly operate on data structures critical to the system
  - Which must be protected by access control protocols
  - If we can do that, then we do not need the RTOS to spawn its own tasks for deferred interrupt handling

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313 of 538

## Interrupt handling /7

- Using the OOD patterns we saw earlier, the deferred part of the ISR would map to a *sporadic* task released by the immediate part of the ISR
- For better responsiveness, schemes such as *slack stealing* or *bandwidth preservation* could be used
  - So that total interference from interrupts is bounded, but a given quota of them may receive full service within replenishment intervals
  - During those intervals, bandwidth preservation retains the unused reserve of execution budget, which can help serve occasional bursts
- These solutions need *specialized* support from the RTOS

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314 of 538

## Fine-grained response time analysis

$R_i$  is a *compositional* term

Its RHS benefits from *composable* terms

$$R_i^{n+1} = B_i + CS1 + C_i + \sum_{j \in hp(i)} \left[ \frac{R_j^n + J_j^A}{T_j} \right] (CS1 + C_j) + TS + CS2 + I_{clock}^{R_i} + I_{extnt}^{R_i}$$

Annotations for the equation above:

- Blocking time (resource access protocol or kernel) →  $B_i$
- "In" context switch →  $CS1$
- "Activation" jitter →  $J_j^A$
- "Out" context switch →  $C_j$
- Time to issue a suspension call →  $TS$
- Interference from the clock →  $I_{clock}^{R_i}$
- Interference from interrupts →  $I_{extnt}^{R_i}$

$$R_i^0 = B_i + CS1 + C_i$$

$$R_i = R_i^n + J^W \leftarrow \text{"Wake-up" jitter}$$

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315 of 538

## Summary

- Programming real-time applications
- RTOS design issues
- Context switch
- Priority levels
- Tick scheduling
- System calls
- Interrupt handling
- Time management

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316 of 538

## Selected readings

- T. Vardanega, J. Zamorano, J.A. de la Puente (2005)  
*On the Dynamic Semantics and the Timing Behavior of Ravenscar Kernels*  
DOI: 10.1023/B:TIME.0000048937.17571.2

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317 of 538